



## UNITED STATES PATENT AND TRADEMARK OFFICE

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/609,502	07/03/2000	Tomoyasu Katsuyama	9281/3698	1658
757 7	7590 08/27/2002		• .	A .*.
BRINKS HOFER GILSON & LIONE			EXAMINER	
	P.O. BOX 10395 CHICAGO, IL 60610		LESPERANCE, JEAN E	
			ART UNIT	PAPER NUMBER
			2674	
			DATE MAILED: 08/27/2002	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	
	09/609,502	KATSUYAMA ET AL.	
Office Action Summary	Examiner	Art Unit	
	Jean E Lesperance	2674	
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with th	e correspondence address	
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period of Failure to reply within the set or extended period for reply will, by statute  - Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).  Status	36(a). In no event, however, may a reply be y within the statutory minimum of thirty (30) will apply and will expire SIX (6) MONTHS for c, cause the application to become ABANDO	e timely filed  days will be considered timely.  rom the mailing date of this communication.  DNED (35 U.S.C. § 133).	
1) Responsive to communication(s) filed on 03.	<i>July 2000</i> .		
2a) ☐ This action is <b>FINAL</b> . 2b) ☑ Th	is action is non-final.		
Since this application is in condition for allowated closed in accordance with the practice under Disposition of Claims			
4)⊠ Claim(s) <u>1-13</u> is/are pending in the application	1.		
4a) Of the above claim(s) is/are withdraw			
5) Claim(s) is/are allowed.			
6)⊠ Claim(s) <u>1-13</u> is/are rejected.			
7) Claim(s) is/are objected to.			
8) Claim(s) are subject to restriction and/o	r election requirement.		
Application Papers			
9) The specification is objected to by the Examine			
10)⊠ The drawing(s) filed on <u>03 July 2000</u> is/are: a)∑			
Applicant may not request that any objection to the		• •	
11) The proposed drawing correction filed on		proved by the Examiner.	
If approved, corrected drawings are required in rep  12) The oath or declaration is objected to by the Ex	•		
Priority under 35 U.S.C. §§ 119 and 120	diffilier.		
13) Acknowledgment is made of a claim for foreign	a priority under 25 H S.C. & 110	2(a) (d) or (f)	
a) ☐ All b) ☐ Some * c) ☐ None of:	r priority under 35 0.5.C. § 11:	5(a)-(d) 01 (1).	
1.☐ Certified copies of the priority document	s have been received		
Certified copies of the priority documents     Certified copies of the priority documents		ation No	
Copies of the certified copies of the prior application from the International Bu     See the attached detailed Office action for a list	rity documents have been rece reau (PCT Rule 17.2(a)).	ived in this National Stage	
14) Acknowledgment is made of a claim for domesti	·		
a) The translation of the foreign language pro		• • • • • • • • • • • • • • • • • • • •	
15)☐ Acknowledgment is made of a claim for domesti			
Attachment(s)			
Notice of References Cited (PTO-892)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  Information Disclosure Statement(s) (PTO-1449) Paper No(s) 6	5) Notice of Inform	nary (PTO-413) Paper No(s) al Patent Application (PTO-152)	

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### **DETAILED ACTION**

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-13 are rejected under 35 U.S.C. 103 (a) as being unpatentable over U.S. Patent # 6,249,362 ('Sato et al.") in view of U.S. Patent # 5,504,538 ("Tsujihara et al.").

As for claims 1 and 10, Sato et al. teach a processed analog signal that is converted into a digital image signal by an analog digital A/D converter 1031; a reference numeral 130 denotes a black level correction circuit for adjusting the black level of the analog image signal outputted from the CCD 110; 131, an analog digital (A/D) converter for converting the analog image signal whose black level is corrected into the digital image signal. Accordingly, Sato et al. teach all the claimed limitations as recited in claim 1 with the exception of providing a blank data generator, an image data combiner and display the output of the image combiner.

However, Tsujihara et al. teach a blanking period (column 13, line 39) corresponding to a blank data generator; a brightness reference signal is added to the video signal blanking period and applied to the first electrode (column 7, lines 7-9) corresponding to an image data combiner; the BRT reference signal from the BRT reference signal generator 7 that is added to the video signal by the adder 5, and the reference signal-added video signal is output (column 11, lines 18-21) corresponding to

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display an output of the image data combiner on said screen.

It would have been obvious to utilize the blanking period, the added signal, and the output image as taught by Tsujihara et al. in the image read system disclosed by Sato et al. because this would provide an adding means for adding a brightness reference signal symmetrically with reference to the black level in the vertical blanking period of the input video signal.

As for claims 2, 7, and 11, Tsujihara et al. teach a variable gain amplifier 17 for feedback control of the contrast (column 8, lines 8-9) corresponding to a variable resistor.

As for claims 3, 8, and 12, Sato et al. teach a signal indicating that the backlight state is detected from the IX information (column 22, lines 61-62) corresponding to an illumination sensor to detect the illumination around a video camera that outputs said analog image signal.

As for claims 4, 9, and 13, Tsujihara et al. teach a BRT reference signal that is added to the pedestal voltage level (black level) of the video signal and the range of upper or lower limits of the added BRT reference signal can be varied according to the type of display device (column 12, lines 44-50).

As for claim 5, Sato et al. teach an electric image signal which is processed with the shading correction enters a gamma correction circuit 43 where contrast of the image is adjusted and the image signal which is presented as ten-bit image data is converted into eight-bit image data. The two images are different.

As for claim 6, Tsujihara et al. teach a bias adjustment adjusting near-black low

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light levels, 0% and 25% black level test signals are emitted, and the white balance of near-black low light levels is adjusting using the bias control signal input from the input terminal (column 10, lines 23-26); blanking period (column 13, line 39) corresponding to a blank data generator; a brightness reference signal is added to the video signal blanking period and applied to the first electrode (column 7, lines 7-9) corresponding to an image data combiner; the BRT reference signal from the BRT reference signal generator 7 that is added to the video signal by the adder 5, and the reference signal-added video signal is output (column 11, lines 18-21) corresponding to display an output of the image data combiner on said screen.

#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jean Lesperance whose telephone number is (703) 308-6413. The examiner can normally be reached on from Monday to Friday between 8:OOAM and 4:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's Supervisor, Richard Hierpe, can be reached on (703) 305-4709.

# Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

#### or faxed to:

(703) 872-9314 (for Technology Center 2600 only)

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the technology Center 2600 Customer Service Office Whose telephone number is (703) 306-0377.

Jean Lesperance

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Date 8-22-2002

RICHARD HJERFE BOODY PATERT EXCLUS

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